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- Package Options Include Plastic Small-Outline (D) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

## description

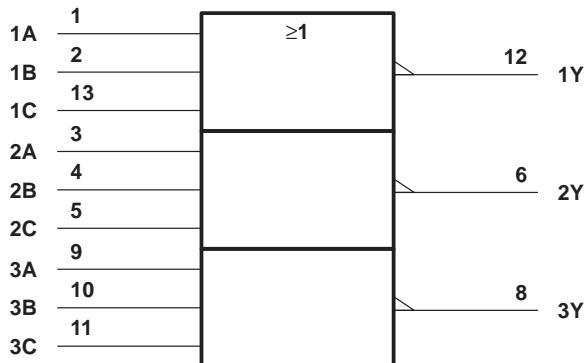
These devices contain three independent 3-input positive-NOR gates. They perform the Boolean functions  $Y = \bar{A} \bullet \bar{B} \bullet \bar{C}$  or  $Y = \bar{A} + \bar{B} + \bar{C}$  in positive logic.

The SN54ALS27A and SN54AS27 are characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN74ALS27A and SN74AS27 are characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

FUNCTION TABLE  
(each gate)

INPUTS			OUTPUT
A	B	C	Y
H	X	X	L
X	H	X	L
X	X	H	L
L	L	L	H

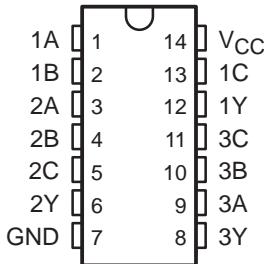
## logic symbol†



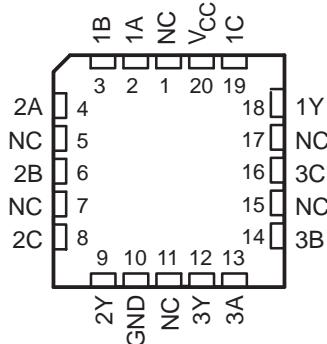
† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for the D, J, and N packages.

SN54ALS27A, SN54AS27 . . . J PACKAGE  
SN74ALS27A, SN74AS27 . . . D OR N PACKAGE  
(TOP VIEW)

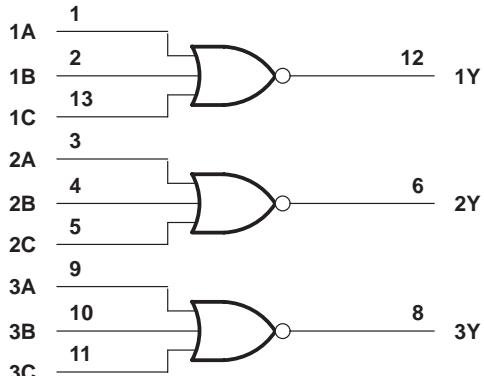


SN54ALS27A, SN54AS27 . . . FK PACKAGE  
(TOP VIEW)



NC – No internal connection

## logic diagram (positive logic)



# SN54ALS27A, SN54AS27, SN74ALS27A, SN74AS27 TRIPLE 3-INPUT POSITIVE-NOR GATES

SDAS112B – APRIL 1982 – REVISED DECEMBER 1994

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage, $V_{CC}$	7 V
Input voltage, $V_I$	7 V
Operating free-air temperature range, $T_A$ : SN54ALS27A SN74ALS27A	–55°C to 125°C 0°C to 70°C
Storage temperature range	–65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

## recommended operating conditions

		SN54ALS27A			SN74ALS27A			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
$V_{CC}$	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
$V_{IH}$	High-level input voltage	2			2			V
$V_{IL}$	Low-level input voltage		0.8 <sup>‡</sup>			0.8		V
			0.7 <sup>§</sup>					
$I_{OH}$	High-level output current			–0.4			–0.4	mA
$I_{OL}$	Low-level output current			4			8	mA
$T_A$	Operating free-air temperature	–55		125	0		70	°C

<sup>‡</sup> Applies over temperature range –55°C to 70°C

<sup>§</sup> Applies over temperature range 70°C to 125°C

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54ALS27A			SN74ALS27A			UNIT
		MIN	TYP <sup>¶</sup>	MAX	MIN	TYP <sup>¶</sup>	MAX	
$V_{IK}$	$V_{CC} = 4.5$ V, $I_I = –18$ mA			–1.5			–1.5	V
$V_{OH}$	$V_{CC} = 4.5$ V to 5.5 V, $I_{OH} = –0.4$ mA	$V_{CC} – 2$			$V_{CC} – 2$			V
$V_{OL}$	$V_{CC} = 4.5$ V	$I_{OL} = 4$ mA		0.25	0.4		0.25	0.4
		$I_{OL} = 8$ mA				0.35	0.5	V
$I_I$	$V_{CC} = 5.5$ V, $V_I = 7$ V			0.1			0.1	mA
$I_{IH}$	$V_{CC} = 5.5$ V, $V_I = 2.7$ V			20			20	μA
$I_{IL}$	$V_{CC} = 5.5$ V, $V_I = 0.4$ V			–0.1			–0.1	mA
$I_O^{\#}$	$V_{CC} = 5.5$ V, $V_O = 2.25$ V	–20	–112	–30	–112			mA
$I_{CCH}$	$V_{CC} = 5.5$ V, $V_I = 0$		0.97	1.8		0.97	1.8	mA
$I_{CCL}$	$V_{CC} = 5.5$ V, $V_I = 4.5$ V		2	4		2	4	mA

<sup>¶</sup> All typical values are at  $V_{CC} = 5$  V,  $T_A = 25$ °C.

<sup>#</sup> The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current,  $I_{OS}$ .

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$ , $C_L = 50 \text{ pF}$ , $R_L = 500 \Omega$ , $T_A = \text{MIN to MAX}^{\dagger}$				UNIT	
			SN54ALS27A		SN74ALS27A			
			MIN	MAX	MIN	MAX		
$t_{PLH}$	A, B, or C	Y	4	26	3	15	ns	
$t_{PHL}$			1	11	1	9		

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>‡</sup>

Supply voltage, $V_{CC}$	.....	7 V
Input voltage, $V_I$	.....	7 V
Operating free-air temperature range, $T_A$ : SN54AS27	.....	-55°C to 125°C
SN74AS27	.....	0°C to 70°C
Storage temperature range	.....	-65°C to 150°C

<sup>‡</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN54AS27			SN74AS27			UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	
$V_{CC}$	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
$V_{IH}$	High-level input voltage	2			2			V
$V_{IL}$	Low-level input voltage			0.8			0.8	V
$I_{OH}$	High-level output current			-2			-2	mA
$I_{OL}$	Low-level output current			20			20	mA
$T_A$	Operating free-air temperature	-55	125	0	0	70	70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54AS27			SN74AS27			UNIT
		MIN	TYP <sup>§</sup>	MAX	MIN	TYP <sup>§</sup>	MAX	
$V_{IK}$	$V_{CC} = 4.5 \text{ V}$ , $I_I = -18 \text{ mA}$			-1.2			-1.2	V
$V_{OH}$	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$ , $I_{OH} = -2 \text{ mA}$	$V_{CC} - 2$			$V_{CC} - 2$			V
$V_{OL}$	$V_{CC} = 4.5 \text{ V}$ , $I_{OL} = 20 \text{ mA}$		0.35	0.5		0.35	0.5	V
$I_I$	$V_{CC} = 5.5 \text{ V}$ , $V_I = 7 \text{ V}$			0.1			0.1	mA
$I_{IH}$	$V_{CC} = 5.5 \text{ V}$ , $V_I = 2.7 \text{ V}$			20			20	μA
$I_{IL}$	$V_{CC} = 5.5 \text{ V}$ , $V_I = 0.4 \text{ V}$			-0.5			-0.5	mA
$I_{O\ddagger}$	$V_{CC} = 5.5 \text{ V}$ , $V_O = 2.25 \text{ V}$	-30	-112		-30	-112		mA
$I_{CCH}$	$V_{CC} = 5.5 \text{ V}$ , $V_I = 0$		4	6.4		4	6.4	mA
$I_{CCL}$	$V_{CC} = 5.5 \text{ V}$ , $V_I = 4.5 \text{ V}$		10.6	17.1		10.6	17.1	mA

<sup>§</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

<sup>†</sup> The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current,  $I_{OS}$ .

# SN54ALS27A, SN54AS27, SN74ALS27A, SN74AS27 TRIPLE 3-INPUT POSITIVE-NOR GATES

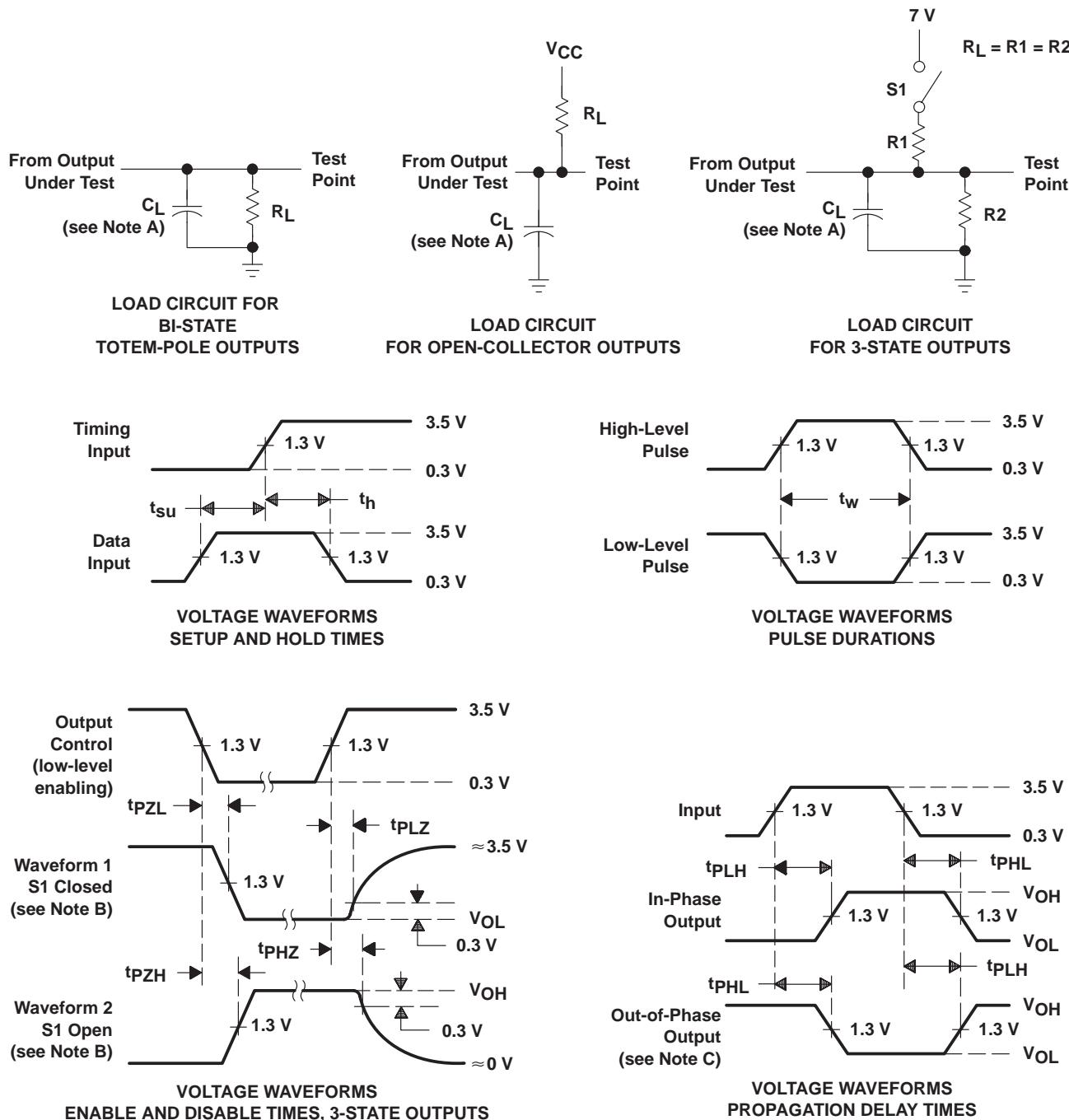
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## switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$ , $C_L = 50 \text{ pF}$ , $R_L = 500 \Omega$ , $T_A = \text{MIN to MAX}^\dagger$				UNIT	
			SN54AS27		SN74AS27			
			MIN	MAX	MIN	MAX		
$t_{PLH}$	A, B, or C	Y	1	6.5	1	5.5	ns	
			1	5	1	4.5		

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

PARAMETER MEASUREMENT INFORMATION  
SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES:

- $C_L$  includes probe and jig capacitance.
- Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- When measuring propagation delay items of 3-state outputs, switch S1 is open.
- All input pulses have the following characteristics: PRR  $\leq$  1 MHz,  $t_r = t_f = 2$  ns, duty cycle = 50%.
- The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms

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