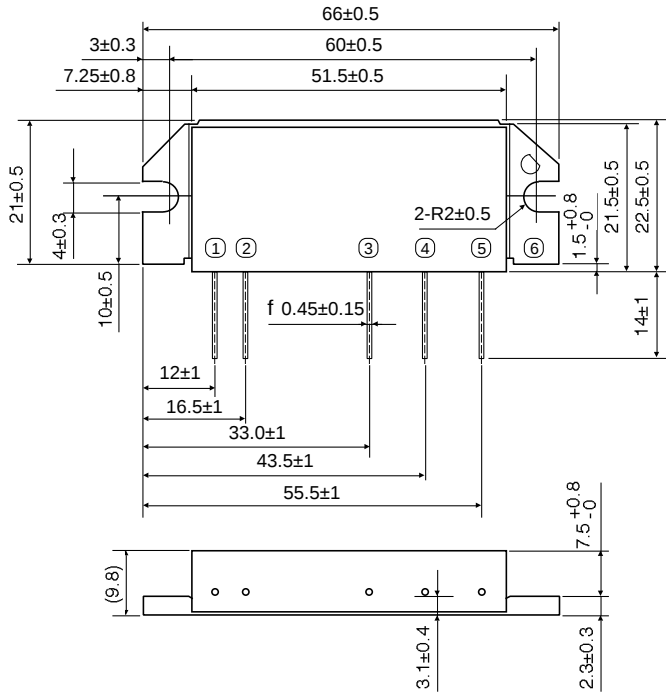


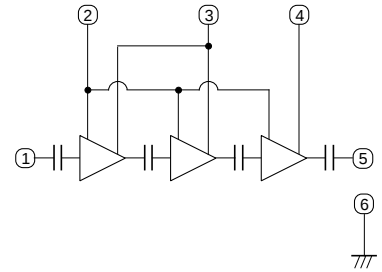
OUTLINE DRAWING

Dimensions in mm



H3

BLOCK DIAGRAM



PIN:

- ①Pin : RF INPUT
- ②VBB : BASE BIAS SUPPLY
- ③VCC1: 1st. DC SUPPLY
- ④VCC2: 2nd. DC SUPPLY
- ⑤Po : RF OUTPUT
- ⑥GND: FIN

ABSOLUTE MAXIMUM RATINGS (Tc=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
Vcc	Supply voltage	ZG=ZL=50W, VBB=8.5V	17	V
VBB	Bias voltage	ZG=ZL=50W, Vcc£13.2V	9	V
Icc	Total current	ZG=ZL=50W	6	A
P _{in} (max)	Input power	ZG=ZL=50W, Vcc£13.2V	300	mW
P _O (max)	Output power	ZG=ZL=50W	25	W
T _C (OP)	Operation case temperature	ZG=ZL=50W	-30 to +110	°C
T _{stg}	Storage temperature		-40 to +110	°C

Note. Above parameters are guaranteed independently.

ELECTRICAL CHARACTERISTICS (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test conditions	Limits		Unit
			Min	Max	
f	Frequency range		380	400	MHz
P _O	Output power	P _{in} =200mW, Vcc=12.5V, VBB=9V, ZG=ZL=50W	13		W
h _T	Total efficiency		15		%
2f _o	2nd. harmonic	Vcc=12.5V, VBB=9V, Po=5W (P _{in} :controlled), ZG=ZL=50W		-25	dBc
3f _o	3rd. harmonic			-30	dBc
r _{in}	Input VSWR			2.8	-
G _p	Power gain		28		dB
IMD3	3rd. internal modulation	Vcc=12.5V, VBB=9V, Po (AVE)=5W		-25	dBc
IMD5	5th. internal modulation	(P _{in} :controlled) 2tone, Df=10kHz, ZG=ZL=50W		-32	dBc
-	Load VSWR tolerance	Vcc=15.2V, VBB=9V, Po=13W (P _{in} :controlled) ZG=50W, Load VSWR=4:1 (All phase)	No degradation or destroy		-
-	Stability	P _{in} =0-300mW, Vcc=10-16V, VBB=9V, Po £ 20W, r _i £ 3.0 (All phase), ZG=50W	No parastic oscillation		Note 1

Note. Above parameters, ratings, limits and test conditions are subject to change.

Note 1. Stability is tested by sampling test (10pcs/Lot)

TYPICAL PERFORMANCE DATA

