

November 2015

# **FOD852**

# 4-Pin DIP Photodarlington Output Optocoupler

#### **Features**

- High Current Transfer Ratio: 1000% Minimum
- · Safety and Regulatory Approvals
  - UL1577, 5,000 VAC<sub>RMS</sub> for 1 Minute
  - DIN EN/IEC60747-5-5

## **Applications**

- · Power Supply Regulators
- Digital Logic Inputs
- Microprocessor Inputs

## **Description**

The FOD852 consists of gallium arsenide infrared emitting diode driving a silicon photodarlington output (with integral base-emitter resistor) in a 4-pin dual in-line package.

## **Functional Block Diagram**

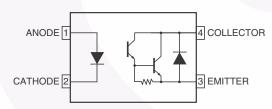


Figure 1. Schematic



Figure 2. Package Outlines

## **Safety and Insulation Ratings**

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter		Characteristics
Installation Classifications per DIN VDE	< 150 V <sub>RMS</sub>	I–IV
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V <sub>RMS</sub>	I–III
Climatic Classification		30/110/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
V	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC	1360	V <sub>peak</sub>
V PR	Input-to-Output Test Voltage, Method B, V <sub>IORM</sub> x 1.875 = V <sub>PR</sub> , 100% Production Test with t <sub>m</sub> = 1 s, Partial Discharge < 5 pC		V <sub>peak</sub>
V <sub>IORM</sub>	Maximum Working Insulation Voltage	850	V <sub>peak</sub>
V <sub>IOTM</sub>	Highest Allowable Over-Voltage	6000	V <sub>peak</sub>
	External Creepage	≥ 7	mm
	External Clearance	≥ 7	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.4	mm
T <sub>S</sub>	Case Temperature <sup>(1)</sup>	175	°C
I <sub>S,INPUT</sub>	Input Current <sup>(1)</sup>	400	mA
P <sub>S,OUTPUT</sub>	Output Power <sup>(1)</sup>	700	mW
R <sub>IO</sub>	Insulation Resistance at T <sub>S</sub> , V <sub>IO</sub> = 500 V <sup>(1)</sup>	> 10 <sup>11</sup>	Ω

#### Note:

1. Safety limit values – maximum values allowed in the event of a failure.

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.  $T_A = 25^{\circ}C$  Unless otherwise specified.

Symbol	Parameter	Value	Units
Total Device			
T <sub>STG</sub>	Storage Temperature	-55 to +125	°C
T <sub>OPR</sub>	Operating Temperature	-30 to +100	°C
T <sub>J</sub>	Junction Temperature	-55 to +100	°C
T <sub>SOL</sub>	Lead Solder Temperature	260 for 10 seconds	°C
P <sub>TOT</sub>	Total Device Power Dissipation	200	mW
Input			
I <sub>F</sub>	Continuous Forward Current	50	mA
V <sub>R</sub>	Reverse Voltage	6	V
P <sub>D</sub>	LED Power Dissipation	70	mW
Output			
V <sub>CEO</sub>	Collector-Emitter Voltage	300	V
V <sub>ECO</sub>	Emitter-Collector Voltage	0.1	V
I <sub>C</sub>	Continuous Collector Current	150	mA
P <sub>C</sub>	Collector Power Dissipation	150	mW

## **Electrical Characteristics**

 $T_A = 25$ °C unless otherwise specified.

## **Individual Component Characteristics**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Input						
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 10 mA		1.2	1.4	V
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 4 V			10	μΑ
C <sub>t</sub>	Terminal Capacitance	V = 0, f = 1 kHz		30	250	pF
Output						
I <sub>CEO</sub>	Collector Dark Current	V <sub>CE</sub> = 200 V, I <sub>F</sub> = 0			200	nA
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 0.1 \text{ mA}, I_F = 0$	300			V
BV <sub>ECO</sub>	Emitter-Collector Breakdown Voltage	$I_E = 10 \mu A, I_F = 0$	0.1			V

### **Transfer Characteristics**

Symbol	DC Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
I <sub>C</sub>	Collector Current	I <sub>F</sub> = 1 mA, V <sub>CE</sub> = 2 V	10	40	150	mA
CTR	Current Transfer Ratio <sup>(2)</sup>	IF = I IIIA, VCE = 2 V	1,000	4,000	15,000	%
V <sub>CE(SAT)</sub>	Collector-Emitter Saturation Voltage	$I_F = 20 \text{ mA}, I_C = 100 \text{ mA}$			1.2	V
f <sub>C</sub>	Cut-Off Frequency	$V_{CE} = 2 \text{ V}, I_{C} = 20 \text{ mA}, R_{L} = 100 \Omega, -3 \text{ dB}$	1	7		kHz
t <sub>R</sub>	Response Time (Rise)	$V_{CE} = 2 \text{ V, } I_{C} = 20 \text{ mA, } R_{L} = 100 \Omega$		100	300	μs
t <sub>F</sub>	Response Time (Fall)	VCE - 2 v, IC - 20 IIIA, IC - 100 32		20	100	μs

#### **Isolation Characteristics**

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Units
V <sub>ISO</sub>	Input-Output Isolation Voltage	$f = 60 \text{ Hz}, t = 1 \text{ minute}, I_{I-O} \le 2 \mu\text{A}$	5000			VAC <sub>RMS</sub>
R <sub>ISO</sub>	Isolation Resistance	$V_{I-O} = 500 V_{DC}$		10 <sup>12</sup>		Ω
C <sub>ISO</sub>	Isolation Capacitance	V <sub>I-O</sub> = 0, f = 1 MHz		0.6	1.0	pf

#### Note:

2. Current Transfer Ratio (CTR) =  $I_C / I_F x$  100%.

## **Typical Electrical/Optical Characteristic Curves**

 $T_A = 25$ °C unless otherwise specified.

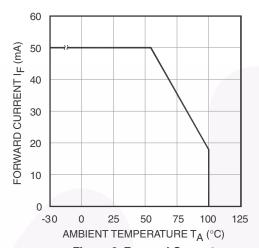
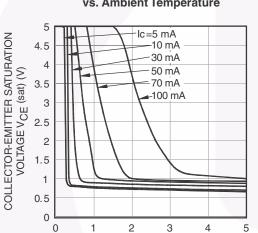


Figure 3. Forward Current vs. Ambient Temperature



FORWARD CURRENT I<sub>F</sub> (mA)

Figure 5. Collector-Emitted Saturation Voltage
vs. Forward Current

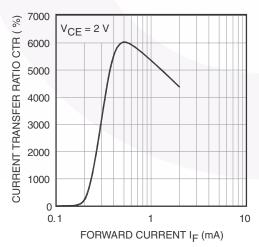


Figure 7. Current Transfer Ratio vs. Forward Current

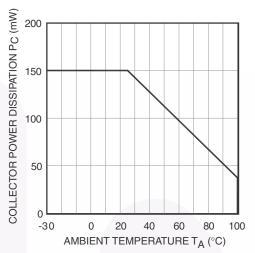


Figure 4. Collector Power Dissipation vs. Ambient Temperature

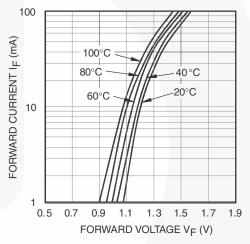


Figure 6. Forward Current vs. Forward Voltage

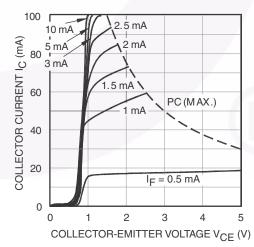


Figure 8. Collector Current vs. Collector-Emitter Voltage

## Typical Electrical/Optical Characteristic Curves (Continued)

 $T_A = 25$ °C unless otherwise specified.

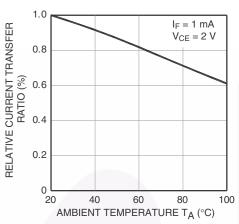
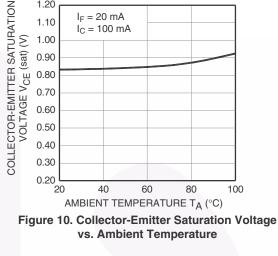


Figure 9. Relative Current Transfer Ratio vs. Ambient Temperature



 $I_F = 20 \text{ mA}$ 

1.20

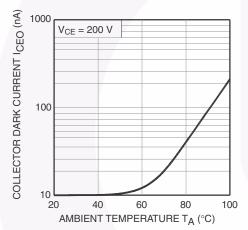


Figure 11. Collector Dark Current vs. Ambient Temperature

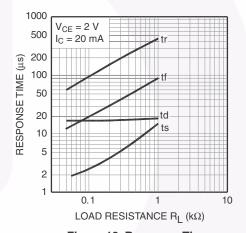


Figure 12. Response Time vs. Load Resistance

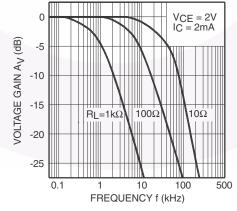


Figure 13. Frequency Response

## **Test Circuits**

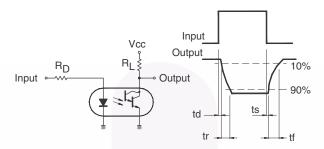


Figure 14. Test Circuit for Response Time

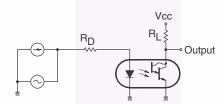
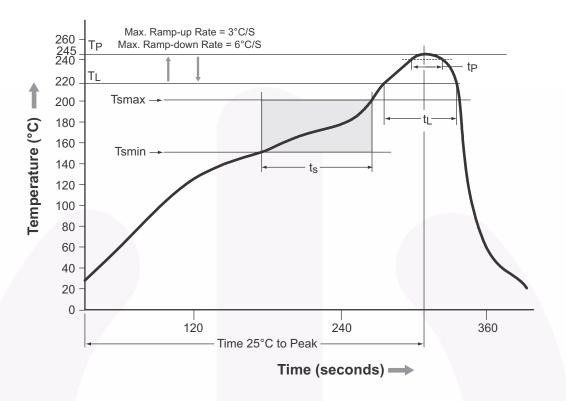


Figure 15. Test Circuit for Frequency Response

## **Reflow Profile**



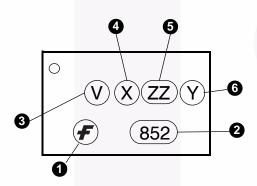
Profile Freature	Pb-Free Assembly Profile		
Temperature Min. (Tsmin)	150°C		
Temperature Max. (Tsmax)	200°C		
Time (t <sub>S</sub> ) from (Tsmin to Tsmax)	60-120 seconds		
Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second max.		
Liquidous Temperature (T <sub>L</sub> )	217°C		
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60-150 seconds		
Peak Body Package Temperature	245°C +0°C / -5°C		
Time (t <sub>P</sub> ) within 5°C of 260°C	30 seconds		
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max.		
Time 25°C to Peak Temperature	8 minutes max.		

Figure 16. Reflow Profile

# **Ordering Information**

Part Number	Package	Packing Method
FOD852	DIP 4-Pin	Tube (100 units per tube)
FOD852S	SMT 4-Pin (Lead Bend)	Tube (100 units per tube)
FOD852SD	SMT 4-Pin (Lead Bend)	Tape and Reel (1,000 units per reel)
FOD852300	DIP 4-Pin, DIN EN/IEC60747-5-5 option	Tube (100 units per tube)
FOD8523S	SMT 4-Pin (Lead Bend), DIN EN/IEC60747-5-5 option	Tube (100 units per tube)
FOD8523SD	SMT 4-Pin (Lead Bend), DIN EN/IEC60747-5-5 option	Tape and Reel (1,000 units per reel)
FOD852300W	DIP 4-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 option	Tube (100 units per tube)

# **Marking Information**



Definiti	ons	
1	Fairchild Logo	
2	Device Number	
3	DIN EN/IEC60747-5-5 Option (only appears on parts ordered with this option)	
4	One-Digit Year Code, e.g., '5'	
5	Two-Digit Work Week, Ranging from '01' to '53'	
6	Assembly Package Code Y = Manufactured in Thailand YA = Manufactured in China	

## **Carrier Tape Specifications**

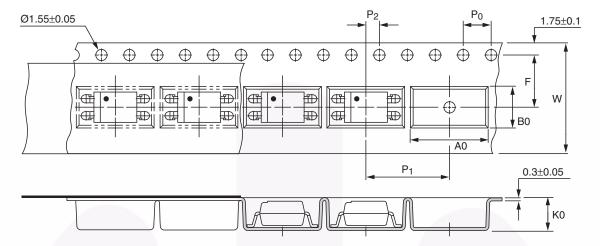
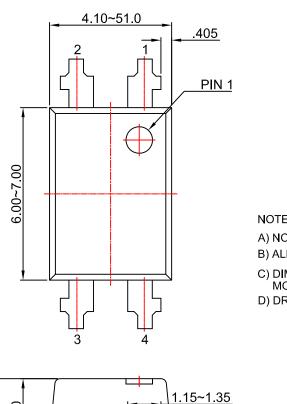


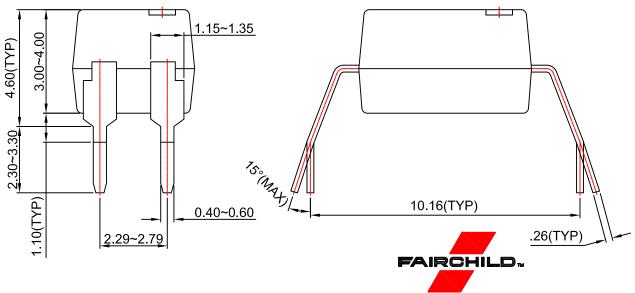
Figure 17. Carrier Tape Specification

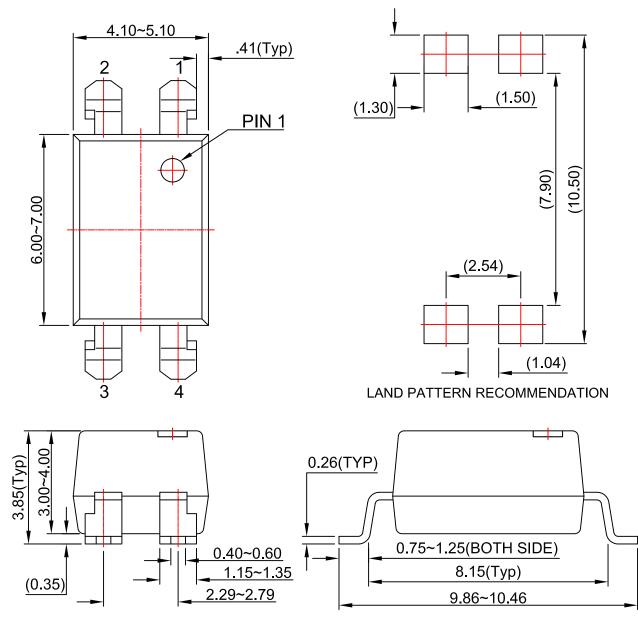
Symbol	Description	Dimensions in mm (inches)
W	Tape wide	16 ± 0.3 (0.63)
P <sub>0</sub>	Pitch of sprocket holes	4 ± 0.1 (0.15)
F P <sub>2</sub>	Distance of compartment	7.5 ± 0.1 (0.295) 2 ± 0.1 (0.079)
P <sub>1</sub>	Distance of compartment to compartment	12 ± 0.1 (0.472)
A0	Compartment	10.45 ± 0.1 (0.411)
В0		5.30 ± 0.1 (0.209)
K0		4.25 ± 0.1 (0.167)



#### NOTES:

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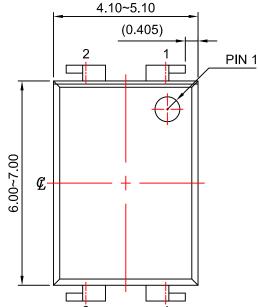




#### NOTES:

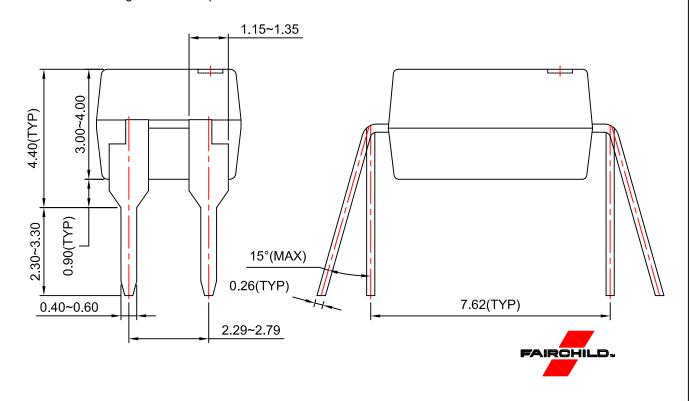
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Deminition of Terms		
Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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