

DM74LS05

Hex Inverters with Open-Collector Outputs

General Description

This device contains six independent gates each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC}(\text{Min}) - V_{OH}}{N_1(I_{OH}) + N_2(I_{IH})}$$

$$R_{MIN} = \frac{V_{CC}(\text{Max}) - V_{OL}}{I_{OL} - N_3(I_{IL})}$$

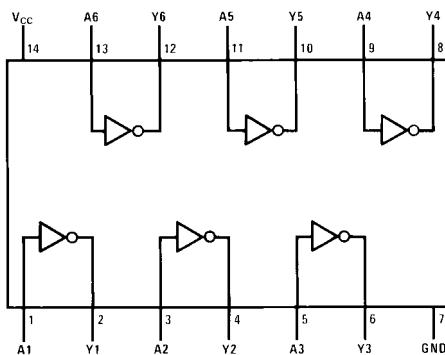
Where:
 $N_1(I_{OH})$ = total maximum output high current for all outputs tied to pull-up resistor
 $N_2(I_{IH})$ = total maximum input high current for all inputs tied to pull-up resistor
 $N_3(I_{IL})$ = total maximum input low current for all inputs tied to pull-up resistor

Ordering Code:

Order Number	Package Number	Package Description
DM74LS05M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
DM74LS05SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
DM74LS05N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

$$Y = \overline{A}$$

Input	Output
A	Y
L	H
H	L

H = HIGH Logic Level
L = LOW Logic Level

Absolute Maximum Ratings (Note 1)

Supply Voltage	7V
Input Voltage	7V
Output Voltage	7V
Operating Free Air Temperature Range	0°C to +70°C
Storage Temperature Range	-65°C to +150°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V_{CC}	Supply Voltage	4.75	5	5.25	V
V_{IH}	HIGH Level Input Voltage	2			V
V_{IL}	LOW Level Input Voltage			0.8	V
V_{OH}	HIGH Level Output Voltage			5.5	V
I_{OL}	LOW Level Output Current			8	mA
T_A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

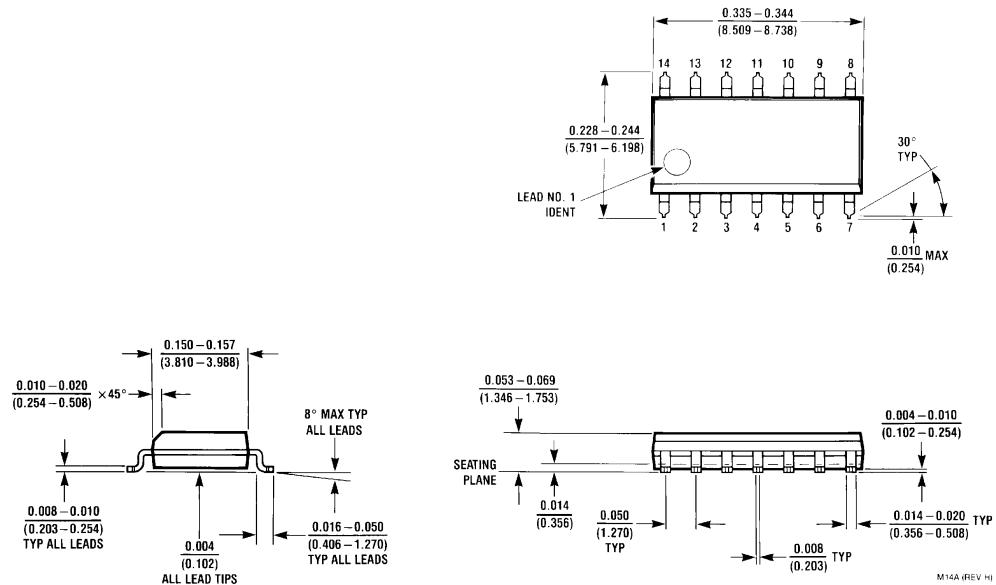
Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
V_I	Input Clamp Voltage	$V_{CC} = \text{Min}$, $I_I = -18 \text{ mA}$			-1.5	V
I_{CEX}	HIGH Level Output Current	$V_{CC} = \text{Min}$, $V_O = 5.5V$ $V_{IL} = \text{Max}$			100	μA
V_{OL}	LOW Level Output Voltage	$V_{CC} = \text{Min}$, $I_{OL} = \text{Max}$		0.35	0.5	V
		$V_{IH} = \text{Min}$		0.25	0.4	
I_I	Input Current @ Max Input Voltage	$V_{CC} = \text{Max}$, $V_I = 7V$			0.1	mA
I_{IH}	HIGH Level Input Current	$V_{CC} = \text{Max}$, $V_I = 2.7V$			20	μA
I_{IL}	LOW Level Input Current	$V_{CC} = \text{Max}$, $V_I = 0.4V$			-0.36	mA
I_{CCH}	Supply Current with Outputs HIGH	$V_{CC} = \text{Max}$		1.2	2.4	mA
I_{CCL}	Supply Current with Outputs LOW	$V_{CC} = \text{Max}$		3.6	6.6	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25^\circ\text{C}$.

Switching Characteristics

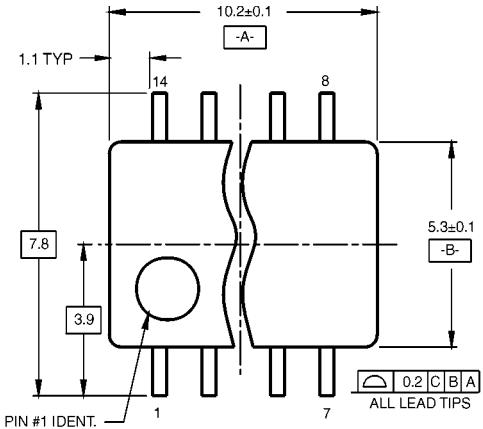
at $V_{CC} = 5V$ and $T_A = 25^\circ\text{C}$

Symbol	Parameter	$R_L = 2 \text{ k}\Omega$				Units	
		$C_L = 15 \text{ pF}$		$C_L = 50 \text{ pF}$			
		Min	Max	Min	Max		
t_{PLH}	Propagation Delay Time LOW-to-HIGH Level Output	6	20	20	45	ns	
t_{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	3	15	4	20	ns	

Physical Dimensions inches (millimeters) unless otherwise noted

14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow
Package Number M14A

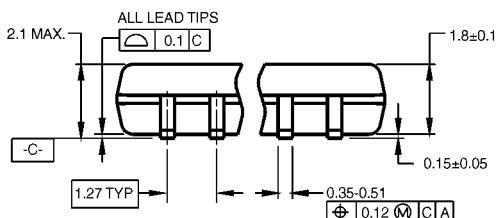
Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



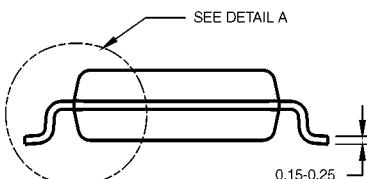
The diagram shows a stepped profile with the following dimensions:

- Top step: 14, 13
- Second step: 9, 8
- Third step: 1, 2
- Fourth step: 6, 7
- Bottom step: 5.01 TYP
- Left side: (2.13 TYP)
- Bottom left: 1.27 TYP
- Bottom right: 0.6 TYP
- Total height: 9.27 TYP

LAND PATTERN RECOMMENDATION



DIMENSIONS ARE IN MILLIMETERS

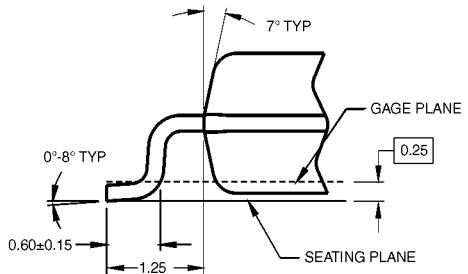


NOTES:

- NOTE:

 - A. CONFORMS TO EIAJ EDR-7320 REGISTRATION, ESTABLISHED IN DECEMBER, 1998.
 - B. DIMENSIONS ARE IN MILLIMETERS.
 - C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

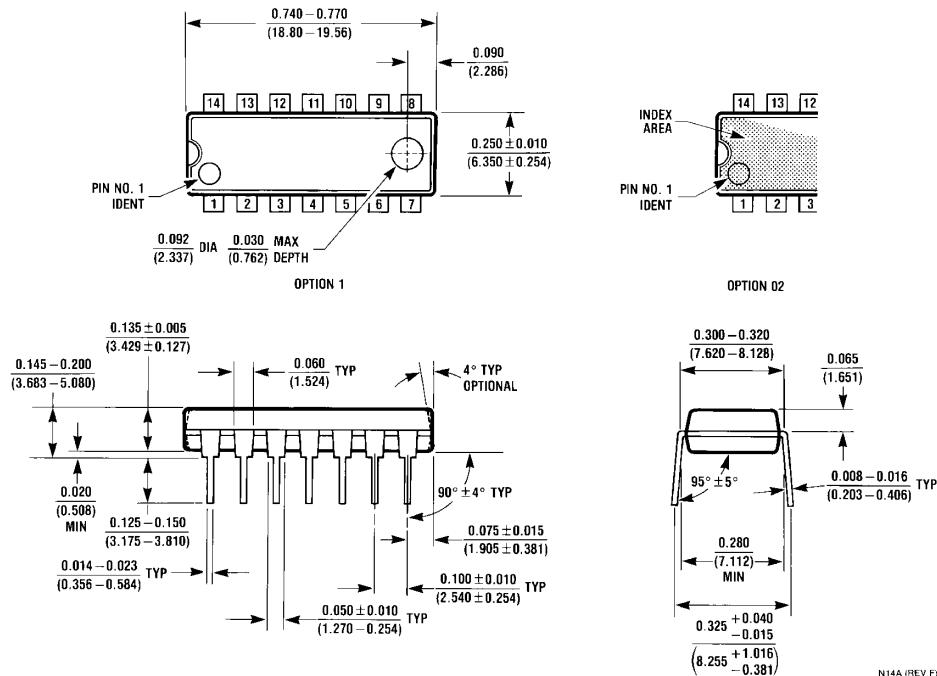
M14DRevB1



DETAIL A

**14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
Package Number M14D**

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide
Package Number N14A

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